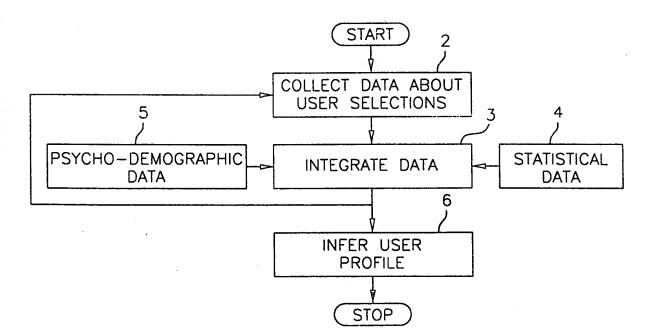
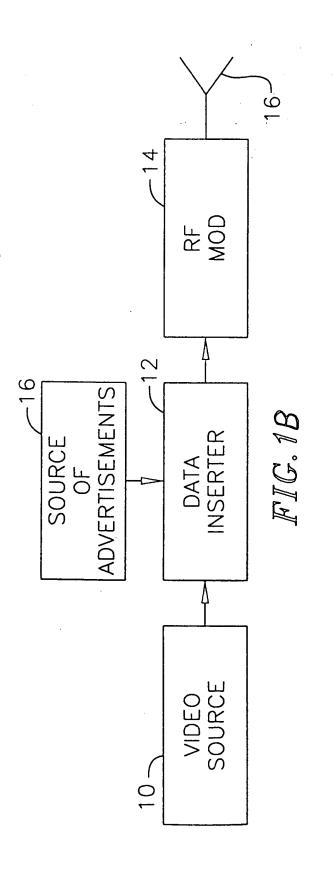
FIG. 1A





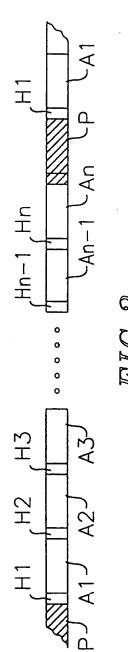
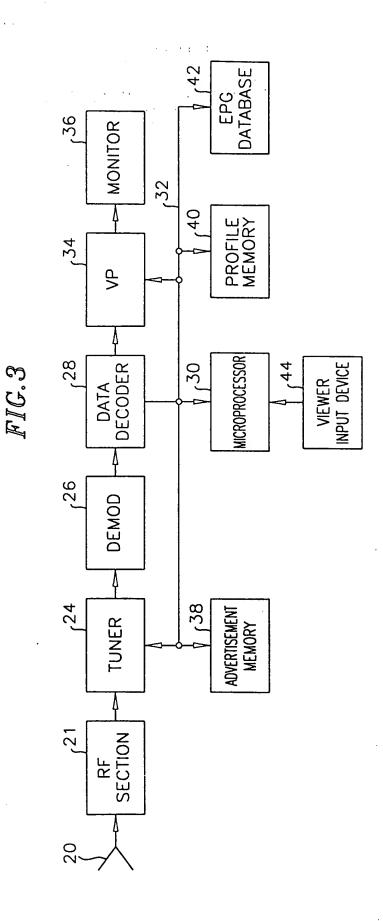


FIG. 2



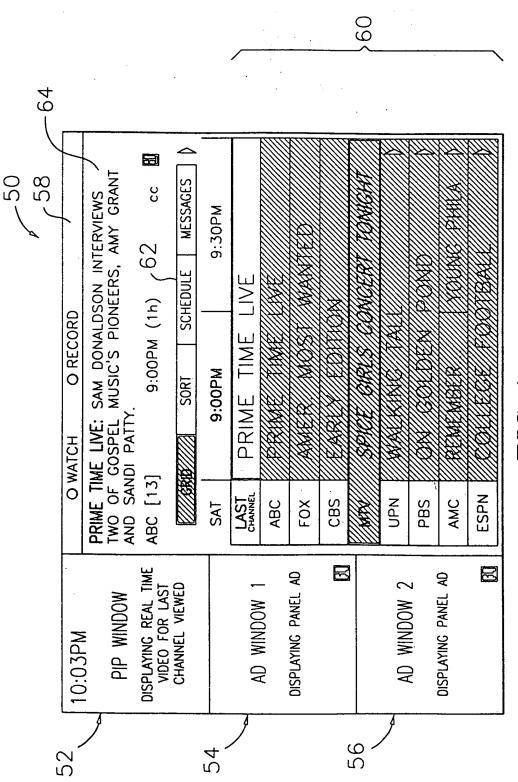
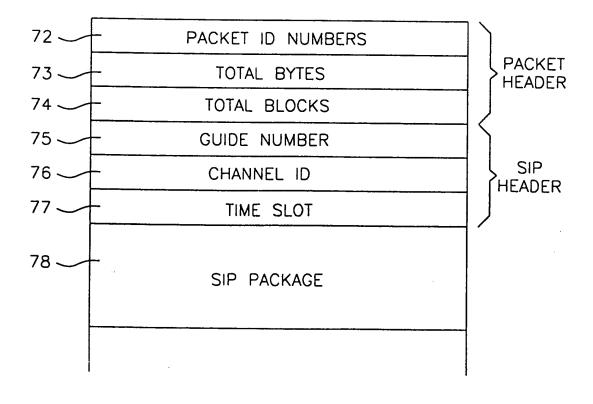
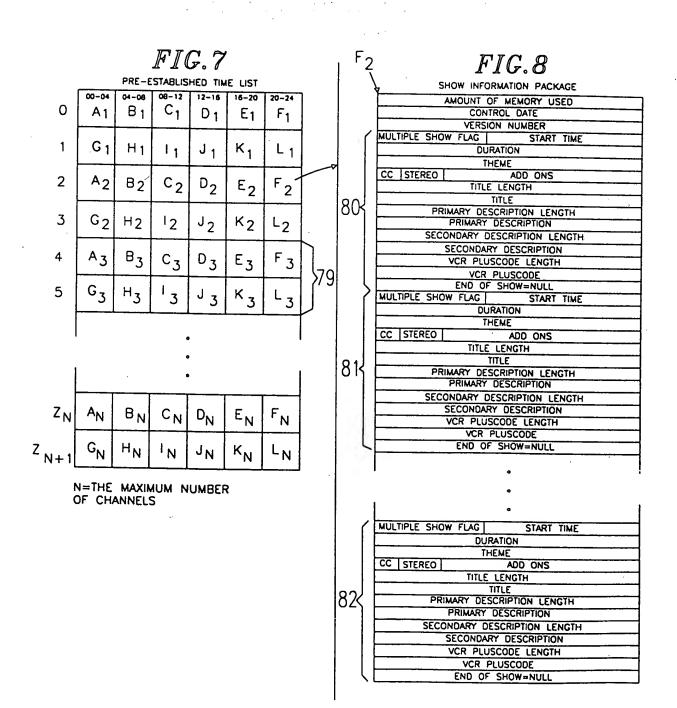


FIG. 4

FIG.5RAM MEMORY DATA BASE M M E A PT RΙ M P E M Ε 0 E S T A B R ST RECORD QUEUE **POINTERS** TO SIMD E EXTENDED THEME FREE STORAGE FOR SIPS, RECORD QUEUE ENTRIES, AND EXTENDED THEME LIST ENTRIES Ť SHOW Ε **LISTS** СМ H A A P М N N Ε CA 0 R NR TA RY 0

FIG.6





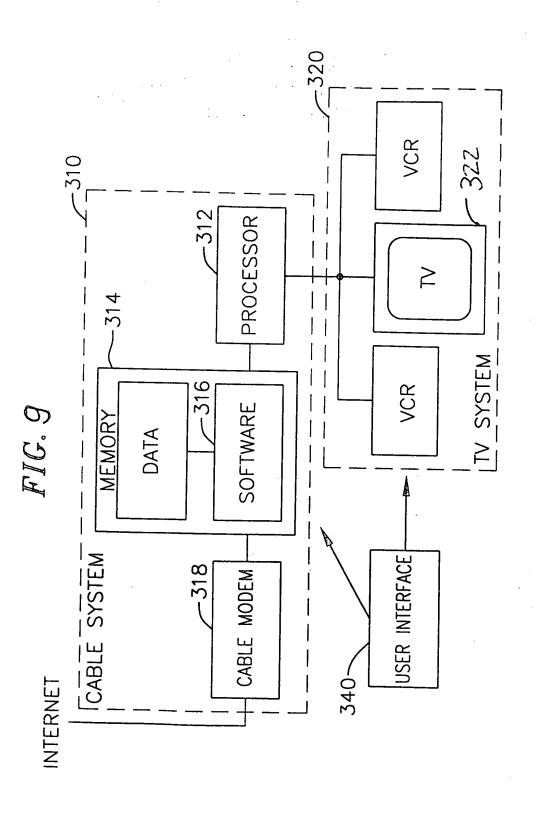


FIG. 10

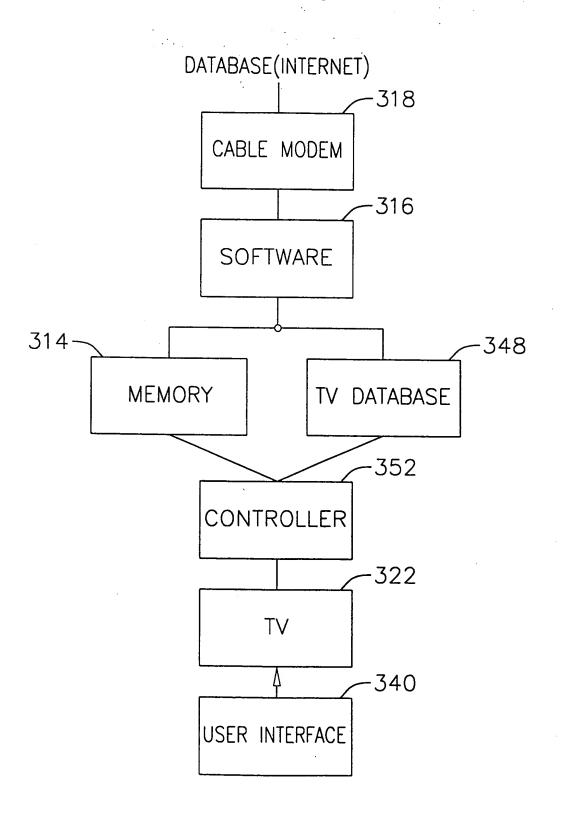


FIG. 11

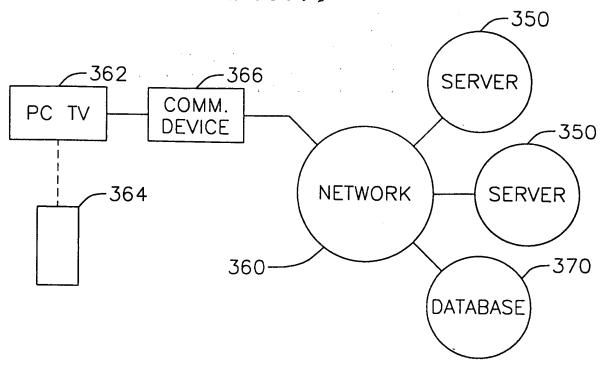
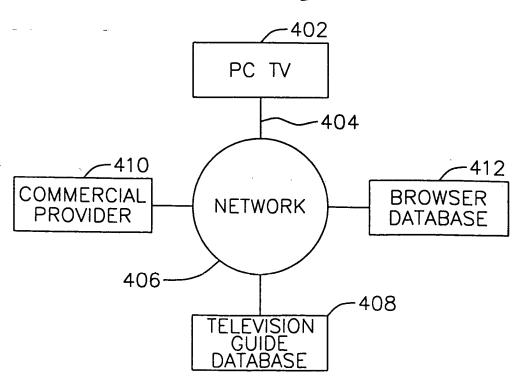
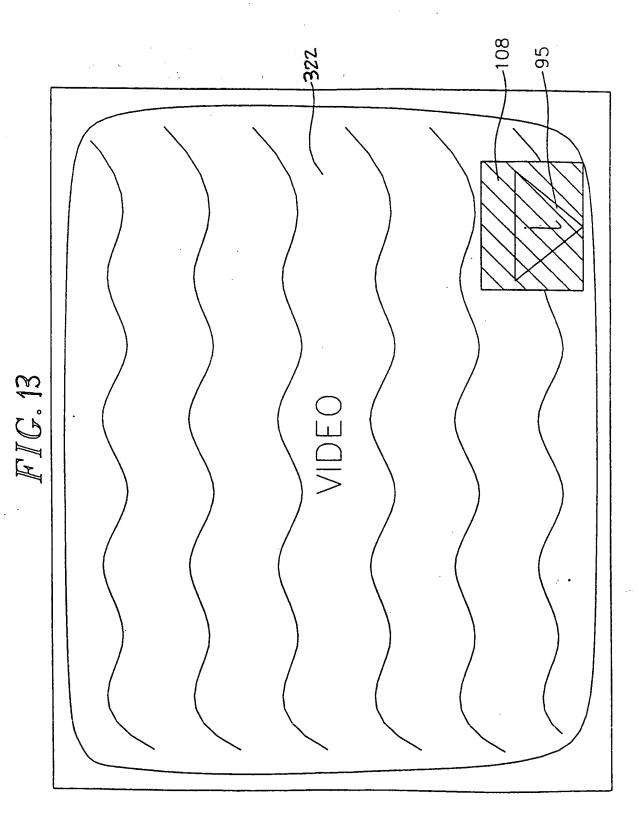


FIG. 12





A.Paris

